Design and Development of Four Channels Fast Counter



For Charge Exchange Diagnostic

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ABSTRACT:

Charge Exchange diagnostic in Aditya Tokamak is used to measure Core Iron temperature of Aditya Plasma. It is estimated using the energy spectrum of Passive Charge Exchange (CX) Neutrals escaping from the ADTYA tokamak. CX-neutrals are detected by Channel Electron Multiplier. The detector, Channel Electron Multipliers is used in counting mode, to collect the number of charge exchange neutrals reaching in a finite interval of time[~ tens of ms] after traveling across the retarding fields of Neutral Particle Analyzer Plates. These counts are further analyzed to get the information regarding Core Ion temperature of Aditya Plasma.

A fast counter is designed and developed to count the number of charge exchange neutral reaching in a finite interval of time detected by Channel Electron Multiplier. It is a four channel 16 bit counter with gating and start facility. The 16 bit counter data is stored in local on-board memory. Different controls and stored data from memory can be transferred to PC using serial communication. The optically isolated external trigger from Loop voltage is used to start counter.

The paper describe the design, development, testing and Graphical User Interface developed using LabVIEW.

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	PC		Counter_Nov 15, 2011_23 □ > File Edit Format View Help 01 01*50 > <	Ext. Trig /Loop Voltage
	Channel Electron PAD A101-Signal Data Multiplier-Sensor Conditioning Data . Input: 4 Channel, 16 Bit counter with maximum count frequency or	f	3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F 1387 03E8 3E7F 270F 1387 03E8 3E7F 270F 1387 03E8 3E7F 270F 1387 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 2710 1387 03E8 3E7F 2710 1387 03E8 3E7F 2710 1387 03E8 3E7F 270F 1388 03E8	*CCKEN 1 to 255 mSec. $36 \times (1 \text{ to } 255)$ mSec. \gg LATCH 1 to 255 mSec. 255 mSec. \gg 1 µSec.
2	 16MHz Controls: a) Gating time: 1 to 255 ms. insteps of 1 ms while Shot ti 50 ms to 12.75 Sec. step of 50 ms. b) Trigger input : Manual or External Optically isolated or through Software (PC) 	t time is 3E7F 270F 3E7F 270F	3E/F 270F 1388 03E8 3E7F 270F 1387 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F 1387 03E8 3E7F 270F 1387 03E8 3E7F 270F 1387 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 2710 1388 03E8 3E7F 2710 1388 03E8 3E7F 2710 1388 03E8 3E7F 2710 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 2710 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F 1388 03E8 3E7F 270F	$RESET \qquad \qquad$









Conclusion: The system is tested with actual data of Charge Exchange Diagnostic in Aditya Tokamk.